# WLM-6581-06

VIA Mark Fanless Panel PC

# User's Manual

P/N: 205G00WLM65810, Version 1.2

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1

## **Copyright Notice**

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## **Safety Precautions**

#### Before getting started, read the following important cautions.

- 1. The WLM-6581-06 may not come equipped with an operating system. An operating system must be loaded first before installing any software into the computer.
- Be sure to ground yourself to prevent static charge when installing the internal components. Use a grounding wrist strap and place all electronic components in any static-shielded devices. Most electronic components are sensitive to static electrical charge.
- 3. Disconnect the power from the WLM-6581-06 before making any installation. Be sure both the system and the external devices are turned OFF. Sudden surge of power could ruin sensitive components. Make sure the WLM-6581-06 is properly grounded.
- 4. The brightness of the flat panel display decreases with usage. However, hours of use vary depending on the application environment.
- 5. Turn OFF the system power before cleaning. Clean the system using a cloth only. Do not spray any liquid cleaner directly onto the system. The WLM-6581-06 may come with or w/o a touch screen. Although the touch screen is chemical resistant, it is recommenced that you spray the liquid cleaner on a cloth first before wiping the screen. In case your system comes without the touch screen, you must follow the same procedure and not spray any cleaner on the flat panel directly.
- 6. Avoid using sharp objects to operate the touch screen. Scratches on the touch screen may cause malfunction or internal failure to the touch screen.
- The WLM-6581-06 is not susceptible to intense shock or vibration. When assembling the WLM-6581-06, make sure it is securely installed.
- 8. If opening the cover for maintenance is a must, only a trained technician is allowed to do so. Integrated circuits on computer boards are sensitive to static electricity. To avoid damaging chips from electrostatic discharge, observe the following precautions:
  - ✓ Before handling a board or integrated circuit, touch an unpainted portion of the system unit chassis for a few seconds. This will help to discharge any static electricity on your body.

- ✓ When handling boards and components, wear a wristgrounding strap, available from most electronic component stores.
- 9. Follow below instructions and notice the caution for replacing and disposing of the RTC Lithium battery CR2032 for safety consideration:

CAUTION: Danger of explosion if battery is incorrectly replaced. Replace only with the same or equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instruction.

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# Version Change History

| Date      | Version | Description   | Remark |
|-----------|---------|---|--------|
| 2007/1/29 | V1.0    | First release                                       |        |
| 2007/5/29 | V1.1    | Change height of<br>533MHz to 63.9 mm               |        |
| 2007/7/23 | V1.2    | Change LCD module<br>Update dimension and<br>weight |        |
|           |         |   |        |
|           |         |   |        |
|           |         |   |        |
|           |         |   |        |
|           |         |   |        |
|           |         |   |        |

## **TABLE OF CONTENTS**

## CHAPTER 1 INTRODUCTION

| 1-1 | About This Manual     | 10 |
|-----|-----------------------|----|
| 1-2 | System Specifications | 12 |
| 1-3 | Safety Precautions    | 17 |

#### CHAPTER 2 HARDWARE CONFIGURATION

| 2-1  | Jumper & Connector Quick Reference Table | 19 |
|------|--|----|
| 2-2  | Component Locations                      | 20 |
| 2-3  | How to Set the Jumpers                   | 21 |
| 2-4  | COM Port Connector                       | 21 |
| 2-5  | RS232/422/485 (COM2) Selection           | 23 |
| 2-6  | Keyboard Connector                       | 24 |
| 2-7  | PS/2 Mouse Connector                     | 24 |
| 2-8  | Power LED Connector                      | 25 |
| 2-9  | Reset Connector                          | 25 |
| 2-10 | Hard Disk Drive LED Connector            | 25 |
| 2-11 | Power Button                             | 26 |
| 2-12 | External Buzzer Connector                | 26 |
| 2-13 | IrDA Connector                           | 26 |
| 2-14 | Inverter Connector                       | 27 |
| 2-15 | VGA Connector                            | 27 |
| 2-16 | LVDS Connector                           | 28 |
| 2-17 | CPU Fan Connector                        | 28 |
| 2-18 | LVDS Panel Voltage Selection             | 29 |
| 2-19 | Universal Serial Bus Connector           | 30 |
| 2-20 | Floppy Disk Drive Connector              | 31 |
| 2-21 | Hard Disk Drive Connector                | 32 |
| 2-22 | Printer Connector                        | 33 |
| 2-23 | Reset/NMI/Clear Watchdog Selection       | 34 |
| 2-24 | LAN Connector                            | 35 |
| 2-25 | ATX Power Connector                      | 35 |
| 2-26 | AT/ATX Power Selection                   | 36 |
| 2-27 | Sound Connector                          | 37 |
| 2-28 | CD Audio-In Connector                    | 37 |
| 2-29 | Clear CMOS Selection                     | 38 |
| 2-30 | Memory Installation                      | 39 |
| 2-31 | TV-Out Connector                         | 39 |
|      |  |    |

7

page

#### CHAPTER 3 SOFTWARE UTILITIES

| 3-1 | Introduction                   | 40 |
|-----|--------------------------------|----|
| 3-2 | VIA 4 IN 1 Service Pack Driver | 41 |
| 3-3 | VGA Driver Utility             | 42 |
| 3-4 | Flash BIOS Update              | 42 |
| 3-5 | LAN Driver Utility             | 45 |
| 3-6 | Sound Driver Utility           | 47 |

#### CHAPTER 4 AWARD BIOS SETUP

| 4-1  | Introduction               | 49 |
|------|----------------------------|----|
| 4-2  | Entering Setup             | 50 |
| 4-3  | The Standard CMOS Features | 51 |
| 4-4  | The Advanced BIOS Features | 55 |
| 4-5  | Advanced Chipset Features  | 59 |
| 4-6  | Integrated Peripherals     | 62 |
| 4-7  | Power Management Setup     | 65 |
| 4-8  | PNP/PCI Configuration      | 68 |
| 4-9  | PC Health Status           | 70 |
| 4-10 | Frequency/Voltage Control  | 71 |
| 4-11 | Load Fail-Safe Defaults    | 71 |
| 4-12 | Load Optimized Defaults    | 72 |
| 4-13 | Password Setting           | 73 |
| 4-14 | Save & Exit Setup          | 74 |
| 4-15 | Exit Without Saving        | 75 |

#### APPENDIX A EXPANSION BUS

| PC/104 Bus Connector Pin Assignment         | 76 |
|---|----|
| PC/104 Plus Bus Connector Pin Assignment    | 78 |
| Compact Flash Card Connector Pin Assignment | 79 |
| PCI Bus Connector Pin Assignment            | 80 |

#### APPENDIX B TECHNICAL SUMMARY

| Block Diagram                              | 82 |
|--|----|
| Interrupt Map                              | 83 |
| RTC & CMOS RAM Map                         | 84 |
| Timer & DMA Channels Map                   | 85 |
| I/O & Memory Map                           | 86 |
| Default Connector Connections Photos       | 87 |
| Resolution Setting for MS XPe Installation | 92 |
|  |    |

## **CHAPTER**



# **INTRODUCTION**

This chapter gives you the information for WLM-6581-06. It also outlines the System specifications.

Section includes:

- About This Manual
- System Specifications
- Safety precautions

Experienced users can skip to chapter 2 on page 2-1 for a Quick Start.

#### **1-1. ABOUT THIS MANUAL**

Thank you for purchasing our WLM-6581-06 enhanced with VGA/Audio/LAN, is fully PC / AT/ATX compatible. The WLM-6581-06 provides faster processing speed, greater expandability and can handle more tasks than before. This manual is designed to assist you how to install and set up the system. It

contains four chapters. The user can apply this manual for configuration according to the following chapters :

#### **Chapter 1 Introduction**

This chapter introduces you to the background of this manual, and the specifications for this system. The final page of this chapter will indicate how to avoid damaging this Embedded Board.

#### Chapter 2 Hardware Configuration

This chapter outlines the component locations and their functions. In the end of this chapter, you will learn how to set jumper and how to configure this card to meet your own needs.

#### Chapter 3 Software Utilities

This chapter contains helpful information for proper installations of the VGA Utility, LAN Utility, Sound Utility, and Flash BIOS Update. It also describes the Watchdog-timer configuration.

#### Chapter 4 Award BIOS Setup

This chapter indicates you how to set up the BIOS configurations.

#### Appendix A Expansion Bus

This Appendix introduces you the expansion bus for PC/104, Compact Flash.

11

#### Appendix B Technical Summary

This section gives you the information about the Technical summary.

## **1-2. SYSTEM SPECIFICATIONS**

| System        |   |
|---------------|---|
| CPU           | VIA Mark Processor 533MHz (FSB 533MHz, support to 800MHz)                                     |
| BIOS          | Phoenix Award PnP BIOS Memory size 2 M bits, with VGA BIOS. Supports S/IO Setup.              |
| Chipset       | Mark + VT82C686B (FSB : 133MHz)   |
| Cache         | Two large (64-KB each, 2-way) on-chip caches  |
| VGA           | Built-in VIA 8606, share system memory, support CRT & LVDS                                    |
| Audio         | AC '97 Codec, Realtek ALC202A with Line-in/Line-out / MIC                                     |
| Memory        | 1 x 144-pin SO-DIMM, support PC100/133 SDRAM up to 512MB                                      |
| I/O           | VT82C686B   |
| IDE           | 1 x 44-pin IDE (DMA 33)   |
| WDT           | 16 level Watchdog timer selectable w/Reset/NMI  |
| H/Monitor     | Monitor CPU Voltage, CPU temperature, and Cooling Fan.  |
| Display       |   |
| Panel         | AUO 6.5" TFT LCD module (640x480, Model no. G065VN01)<br>Brightness: 500 nits                 |
| Touch         |   |
| Controller    | Pen Mount 9026B control board (RS232 interface)   |
| Screen        | AMT, Resistive type, AD-09501 (4-wire)  |
| External I/O  | & Expansion   |
| Serial Port   | 2 x COM ports, COM1 for RS232, COM2 for RS232/422/485<br>( for 16550 compatible type of UART) |
| Parallel Port | Parallel (SPP / EPP / ECP) x 1  |
| IrDA          | 1 (SIR)   |
| Keyboard      | Mini DIN connector or external connector  |
| Mouse         | Mini DIN connector (selectable w / KB) or Y-cable   |
| LAN           | Realtek RTL8139D (10/100Mbps), Support Wake-on-LAN  |
|               | wtih ATX power  |
| Audio         | AC '97 Codec, Realtek ALC202A with Line-in/Line-out / MIC                                     |
| USB           | 2 x USB 1.1   |
| IDE Interface | 44-pin IDE (DMA 33) x 1   |
|               | compact flash slot (IDE) x 1  |
| FUD Interface | 2 X1  |
| Bus Interface | 800MHz, a PC/104 stack-through module is a must)  |

#### **Mechanical & Environmental**

Power supply adapter Operating Temperature Operation Relative Humidity Storage Temperature Storage Relative Humidity Dimensions (L x W x D) (533MHz) Dimensions (L x W x D) (800mHz) Weight (533MHz) Weight (800MHz) 5V/5A, 12V/2A, AT power 0°~45°C (fanless) 20~90%, non-condensing -30°~70°C 20~95%, non-condensing 207.6 x 147.5 x 63.9 mm 207.6 x 147.5 x 75.7 mm 1.2 kg 1.25 kg

#### **System View**

**Outline Drawing** Front side



533MHz rear side





800MHz rear side





## Unpacking

After unpacking the shipping carton, you should find these standard items:

- The WLM-6851-05 Panel PC
- Accessory box including the followings:
  - FDD Cable X 1
  - IDE Cable x 1

- Print + RS232 Cable x 1
- PS/2 Cable x 1
- Power cable x 1
- Sound cable x 1
- TV-out cable x 1
- Jumper (2.00 mm) x 8
- AC-DC adapter x 1
- AC power cord x 1
- USB CABLE;TWO PORT x 1
- PS2 Y cable x 1
- Stylus x 1
- CD-ROM (drivers and user manual) x 1

Inspect all the items. If any item is damaged or missing, notify your dealer immediately.

#### **1-3. SAFETY PRECAUTIONS**

Follow the messages below to avoid your systems from damage:

- 1. Keep your system away from static electricity on all occasions.
- 2. Prevent electric shock. Don't touch any components of this card when the card is power-on. Always disconnect power when the system is not in use.
- 3. Disconnect power when you change any hardware devices. For instance, when you connect a jumper or install any cards, a surge of power may damage the electronic components or the whole system.





# \*\* QUICK START \*\*

Helpful information describes the jumper & connector settings, and component locations.

This section includes:

- Jumper & Connector Quick Reference Table
- Component Locations
- Configuration and Jumper settings
- Connector's Pin Assignments

## 2-1. JUMPER & CONNECTOR QUICK REFERENCE TABLE

| COM Connector                      | COM1, COM2    |
|------------------------------------|---------------|
| RS232/422/485 (COM2) Selection     | JP5           |
| Keyboard Connector                 | KB1           |
| Mouse Connector                    | MS1           |
| Power LED Connector                | J1 (1-3)      |
| Reset Connector                    | J1 (9-10)     |
| Hard Disk Drive LED Connector      | J1(2-4)       |
| Power Button                       | J1(5-6)       |
| External Buzzer Connector          | J1(7-8)       |
| IrDA Connector                     | IR1           |
| Inverter Connector                 | INV1          |
| VGA Connector                      | VGA1          |
| LVDS Connector                     | LVDS1         |
| LVDS Panel Voltage Selection       | JP13          |
| Universal Serial Bus Connector     | USB1,USB2     |
| CPU Fan Connector                  | FAN1          |
| Floppy Disk Drive Connector        | FDD1          |
| Hard Disk Drive Connector          | IDE1          |
| Printer Connector                  | LPT1          |
| Reset/NMI/Clear Watchdog Selection | JP4           |
| LAN Connector                      | LAN1          |
| ATX Power Connector                | CN1           |
| AT/ATX Power Selection             | JP2, JP6, JP7 |
| Sound Connector                    | CN2           |
| CD Audio-in Connector              | CD_IN1        |
| Clear CMOS Selection               | JP8           |
| Memory Installation                | DIMM1         |
| TV-Out Connector                   | JP1           |

-

19

## 2-2. COMPONENT LOCATIONS



WLM-6581-06 Connector, Jumper and Component locations

#### 2-3. HOW TO SET THE JUMPERS

You can configure your board by setting the jumpers. Jumper is consists of two or three metal pins with a plastic base mounted on the card, and by using a small plastic "cap", Also known as the jumper cap (with a metal contact inside), you are able to connect the pins. So you can set-up your hardware configuration by "opening" or "closing" pins.

The jumper can be combined into sets that called jumper blocks. When the jumpers are all in the block, you have to put them together to set up the hardware configuration. The figure below shows how this looks like.

#### 2-4. COM PORT CONNECTOR

There are two COM ports enhanced in this board namely: COM1, COM2. COM1is fixed for RS-232, while COM2 is selectable for RS-232/422/485.

#### COM1 : COM1 Connector

The COM1 Connector assignments are as follows :

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | DCD1       |
| 2   | RX1        |
| 3   | TX1        |
| 4   | DTR1       |
| 5   | GND        |
| 6   | DSR1       |
| 7   | RTS1       |
| 8   | CTS1       |
| 9   | RI1        |
| 10  | NC         |



#### COM2 : COM2 Connector

The COM2 Connector assignments are as follows :

| DIN   | ASSIGNMENT |        |        |
|-------|------------|--------|--------|
| 1 110 | RS-232     | RS-422 | RS-485 |
| 1     | DCD2       | TX-    | TX-    |
| 2     | RX2        | TX+    | TX+    |
| 3     | TX2        | RX+    | RX+    |



| 4  | DTR2 | RX-  | RX- |
|----|------|------|-----|
| 5  | GND  | GND  | GND |
| 6  | DSR2 | RTS- | NC  |
| 7  | RTS2 | RTS+ | NC  |
| 8  | CTS2 | CTS+ | NC  |
| 9  | RI2  | CTS- | NC  |
| 10 | NC   | NC   | NC  |

## 2-5. RS232/422/485 (COM2) SELECTION

JP5: RS-232/422/485 Selection

COM2 is selectable for RS-232, 422, 485 function. The jumper settings are as follows :

| COM 2<br>FUNCTION | JUMPER SETTING<br>(pin closed) | JUMPER<br>ILLUSTRATION   |
|-------------------|--------------------------------|--|
| RS-232            | Open                           | <sup>2</sup> 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0   |
| RS-422            | 1-2, 3-4, 9-10                 | <sup>2</sup><br>1<br><b>1</b><br><b>1</b><br><b>1</b><br><b>1</b><br><b>1</b><br><b>1</b><br><b>1</b><br><b>1</b><br><b>1</b><br><b></b> |
| RS-485            | 1-2, 5-6, 9-10                 | <sup>2</sup><br>1<br>JP5   |

\*\*\* Manufactory default --- RS-232.

#### 2-6. KEYBOARD CONNECTOR

**KB1** : PC/AT Keyboard Connector, support Y-cable The jumper settings are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | KBDATA     |
| 2   | MSDATA     |
| 3   | GND        |
| 4   | KBVCC      |
| 5   | KBCLK      |
| 6   | MSCLK      |



#### 2-7. PS/2 MOUSE CONNECTOR

**MS1** : PS/2 Mouse Connector The pin assignments are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | MSCLK      |
| 2   | MSDATA     |
| 3   | GND        |
| 4   | MSVCC      |



#### 2-8. POWER LED CONNECTOR

**J1 (1-3)** : Power LED Connector The pin assignment is as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | VCC        |
| 3   | GND        |

#### **2-9. RESET CONNECTOR**

J1 (9-10) : Reset Connector.

The pin assignments are as follows :

| PIN | ASSIGNMENT |
|-----|------------|
| 9   | GND        |
| 10  | RESET      |

#### 2-10. HARD DISK DRIVE LED CONNECTOR

**J1(2-4)** : Hard Disk Drive LED Connector The pin assignments are as follows :

| PIN | ASSIGNMENT |
|-----|------------|
| 2   | VCC        |
| 4   | HDD_LED    |

|   | Power LED |    |
|---|-----------|----|
| 2 |           | 10 |
| 1 |           | 9  |
|   | J1        |    |

|   | Re | ese | t  |  |    |
|---|----|-----|----|--|----|
| 2 |    |     |    |  | 10 |
| 1 |    |     |    |  | 9  |
|   |    |     | 11 |  |    |

|   | HDD LED |    |
|---|---------|----|
| 2 |         | 10 |
| 1 |         | 9  |
|   | J1      |    |

#### 2-11. POWER BUTTON

**J1 (5-6)** : Power Button The pin assignments are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 5   | GND        |
| 6   | PW_BN      |

#### 2-12. EXTERNAL BUZZER CONNECTOR

**J1 (7-8)** : External Buzzer Connector The pin assignments are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 7   | E_BUZ      |
| 8   | VCC        |

## 2-13. IRDA CONNECTOR

**IR1** : IrDA (SIR) Connector The pin assignments are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | VCC        |
| 2   | NC         |
| 3   | IRRX       |
| 4   | GND        |
| 5   | IRTX       |





| 1 |  |    |   | 5 |
|---|--|----|---|---|
|   |  | IR | 1 | • |

INV1 : Inverter Connector

The pin assignment is as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | VCC12      |
| 2   | GND        |
| 3   | VCC        |
| 4   | GND        |
| 5   | ENABKL     |

## **2-15. VGA CONNECTOR**

VGA1 : VGA Connector

The pin assignments are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | RED        |
| 2   | GREEN      |
| 3   | BLUE       |
| 4   | NC         |
| 5   | GND        |
| 6   | GND        |
| 7   | GND        |
| 8   | GND        |
| 9   | NC         |
| 10  | GND        |
| 11  | NC         |
| 12  | SPD2       |
| 13  | HSYNC      |
| 14  | VSYNC      |
| 15  | SPCLK2     |



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INV1

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## 2-16. LVDS CONNECTOR

LVDS1 : LVDS Connector

The pin assignments are as follows :

| PIN | ASSIGNMENT | PIN | ASSIGNMENT |
|-----|------------|-----|------------|
| 1   | LVDSVCC    | 16  | YCP        |
| 2   | GND        | 17  | YCM        |
| 3   | ZCM        | 18  | GND        |
| 4   | ZCP        | 19  | Y2P        |
| 5   | GND        | 20  | Y2M        |
| 6   | Z2M        | 21  | GND        |
| 7   | Z2P        | 22  | Y1P        |
| 8   | GND        | 23  | Y1M        |
| 9   | Z1M        | 24  | GND        |
| 10  | Z1P        | 25  | YOP        |
| 11  | GND        | 26  | YOM        |
| 12  | GND        | 27  | GND        |
| 13  | ZOP        | 28  | GND        |
| 14  | ZOM        | 29  | LVDSVCC    |
| 15  | GND        | 30  | LVDSVCC    |



## 2-17. CPU FAN CONNECTOR

**FAN1** : CPU Fan Connector The pin assignments are as follows:



| PIN | ASSIGNMENT |
|-----|------------|
| 1   | GND        |
| 2   | +12V       |
| 3   | CPUFAN     |

| $\sim$ | C | ٦ |
|--------|---|---|
| · )    | 5 | L |
| 1.     | ( | 1 |
| _      | ~ | - |

## 2-18. LVDS PANEL VOLTAGE SELECTION

**JP13** : LVDS Panel Voltage Selection The voltage selection are as follows:

| voltage<br>SELECTION | JUMPER SETTINGS<br>(pin closed) | JUMPER<br>ILLUSTRATION    |
|----------------------|---------------------------------|---------------------------|
| 12V VCC              | 1-2                             | 6 5<br>2 1<br>JP13        |
| 5V VCC               | 3-4                             | 6 5<br>2 1<br><b>JP13</b> |
| 3.3V VCC             | 5-6                             | 6 • 5<br>2 • • 1<br>JP13  |

\*\*\*Manufacturing Default - No default.

#### 2-19. UNIVERSAL SERIAL BUS CONNECTOR

**USB1** : Universal Serial Bus Connector The pin assignments are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | USBVCC     |
| 2   | USBPØ-     |
| 3   | USBPØ+     |
| 4   | GND        |
| 5   | USBVCC     |
| 6   | USBP1-     |
| 7   | USBP1+     |
| 8   | GND        |



**USB2** : Universal Serial Bus Connector The pin assignments are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | USBVCC     |
| 2   | USBDT2-    |
| 3   | USBDT2+    |
| 4   | GND        |
| 5   | GND        |
| 6   | USBVCC     |
| 7   | USBDT3-    |
| 8   | USBDT3+    |
| 9   | GND        |
| 10  | GND        |



#### 2-20. FLOPPY DISK DRIVE CONNECTOR

**FDD1** : Floppy Disk Drive Connector

You can use a 34-pin daisy-chain cable to connect two FDDs. On one end of this cable there is a 34-pin flat cable to attach the FDD on the board, the other side attaches to two FDDs. The pin assignments are as follows :

| FDD1 |            |     |            |  |  |  |
|------|------------|-----|------------|--|--|--|
| PIN  | ASSIGNMENT | PIN | ASSIGNMENT |  |  |  |
| 1    | GND        | 2   | DRVDENO    |  |  |  |
| 3    | GND        | 4   | NC         |  |  |  |
| 5    | GND        | 6   | DRVDEN1    |  |  |  |
| 7    | GND        | 8   | INDEX      |  |  |  |
| 9    | GND        | 10  | MTRO       |  |  |  |
| 11   | GND        | 12  | DS1        |  |  |  |
| 13   | GND        | 14  | DS0        |  |  |  |
| 15   | GND        | 16  | MTR1       |  |  |  |
| 17   | GND        | 18  | DIR        |  |  |  |
| 19   | GND        | 20  | STEP       |  |  |  |
| 21   | GND        | 22  | WDATA      |  |  |  |
| 23   | GND        | 24  | WGATE      |  |  |  |
| 25   | GND        | 26  | TRKO       |  |  |  |
| 27   | GND        | 28  | WRPRT      |  |  |  |
| 29   | GND        | 30  | RDATA      |  |  |  |
| 31   | GND        | 32  | HDSEL      |  |  |  |
| 33   | GND        | 34  | DSKCHG     |  |  |  |

#### 2-21. HARD DISK DRIVE CONNECTOR

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**IDE1** : Hard Disk Drive Connector The pin assignments are as follows:

| 1 🗆 🗆 🗆 | 1          |     |            |  |  |  |
|---------|------------|-----|------------|--|--|--|
| IDE1    |            |     |            |  |  |  |
| PIN     | ASSIGNMENT | PIN | ASSIGNMENT |  |  |  |
| 1       | IDERST     | 2   | GND        |  |  |  |
| 3       | PDD7       | 4   | PDD8       |  |  |  |
| 5       | PDD6       | 6   | PDD9       |  |  |  |
| 7       | PDD5       | 8   | PDD10      |  |  |  |
| 9       | PDD4       | 10  | PDD11      |  |  |  |
| 11      | PDD3       | 12  | PDD12      |  |  |  |
| 13      | PDD2       | 14  | PDD13      |  |  |  |
| 15      | PDD1       | 16  | PDD14      |  |  |  |
| 17      | PDD0       | 18  | PDD15      |  |  |  |
| 19      | GND        | 20  | NC         |  |  |  |
| 21      | PDDREQ     | 22  | GND        |  |  |  |
| 23      | PDIOWJ     | 24  | GND        |  |  |  |
| 25      | PDIORJ     | 26  | GND        |  |  |  |
| 27      | PDRDY      | 28  | PULL LOW   |  |  |  |
| 29      | PDDACKJ    | 30  | GND        |  |  |  |
| 31      | IDE_IRQ14  | 32  | NC         |  |  |  |
| 33      | PDA1       | 34  | PD33_66J   |  |  |  |
| 35      | PDA0       | 36  | PDA2       |  |  |  |
| 37      | PDCS1J     | 38  | PDCS3J     |  |  |  |
| 39      | HD_LED1J   | 40  | GND        |  |  |  |
| 41      | VCC        | 42  | VCC        |  |  |  |
| 43      | GND        | 44  | NC         |  |  |  |

-

## **2-22. PRINTER CONNECTOR**

LPT1 : Printer Connector

As to link the Printer to the card, you need a cable to connect both DB25 connector and parallel port. The pin assignments are as follows :

| LPT1 |            |     |            |  |  |  |  |
|------|------------|-----|------------|--|--|--|--|
| PIN  | ASSIGNMENT | PIN | ASSIGNMENT |  |  |  |  |
| 1    | STROBEJ    | 14  | P_AFD      |  |  |  |  |
| 2    | PPD0       | 15  | P_ERR      |  |  |  |  |
| 3    | PPD1       | 16  | P_INIT     |  |  |  |  |
| 4    | PPD2       | 17  | P_SLIN     |  |  |  |  |
| 5    | PPD3       | 18  | GND        |  |  |  |  |
| 6    | PPD4       | 19  | GND        |  |  |  |  |
| 7    | PPD5       | 20  | GND        |  |  |  |  |
| 8    | PPD6       | 21  | GND        |  |  |  |  |
| 9    | PPD7       | 22  | GND        |  |  |  |  |
| 10   | P-ACK      | 23  | GND        |  |  |  |  |
| 11   | P_BUSY     | 24  | GND        |  |  |  |  |
| 12   | P_PE       | 25  | GND        |  |  |  |  |
| 13   | P_SLCT     | 26  | NC         |  |  |  |  |

33

## 2-23. RESET/NMI/CLEAR WATCHDOG SELECTION

**JP4**: Reset/NMI/Clear Watchdog Selection The selections are as follows:

| FUNCTION       | JUMPER SETTING<br>(pin closed) | JUMPER<br>ILLUSTRAT<br>ION               |
|----------------|--------------------------------|--|
| RESET          | 1-2                            | <sup>2</sup> 1000<br>10005<br><b>JP4</b> |
| NMI            | 3-4                            | <sup>2</sup> <b>1 1 1 1 1 1 1 1 1 1</b>  |
| Clear Watchdog | 5-6                            | <sup>2</sup><br>1<br>JP4                 |

\*\*\*Manufacturing Default is set as Reset.

## 2-24. LAN CONNECTOR

LAN1 : LAN Connector

The pin assignments are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | TX+        |
| 2   | TX-        |
| 3   | RX+        |
| 4   | NC         |
| 5   | NC         |
| 6   | RX-        |
| 7   | NC         |
| 8   | NC         |



#### 2-25. ATX POWER CONNECTOR

**CN1** : Power Connector

The pin assignments are as follows :

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | VCC        |
| 2   | VCC        |
| 3   | GND        |
| 4   | GND        |
| 5   | +12V       |
| 6   | +5V SB     |
| 7   | VCC        |
| 8   | GND        |
| 9   | PS_ON      |
| 10  | -12V       |



#### 2-26. AT/ATX POWER SELECTION

JP2, JP6, JP7 : AT/ATX Power Selection The selections are as follows:

| POWER SELECTION | JUMPER SETTING<br>(pin closed) |       |       | JUMPER       |                 |              |
|-----------------|--------------------------------|-------|-------|--------------|-----------------|--------------|
|                 | JP2                            | JP6   | JP7   | ILLUSIKATION |                 |              |
| ATX             | Open                           | Open  | Close | □□¹<br>JP2   | 1□□<br>JP6      | <sup>1</sup> |
| AT              | Close                          | Close | Open  | IP2          | <sup>1</sup> II | ₁□□<br>JP7   |

\*\*\*Manufacturing Default: ATX

As a reminder, when you choose to use the ATX function, please be sure to set the corresponding configuration found in BIOS setup such as:

 I
 I

nside the "POWER MANAGEMENT" setting, set the ACPI function to enable.
# 2-27. SOUND CONNECTOR

CN2 : Sound Connector

This connector is used to connect the microphone, line-in, and line-out through our adapter card. The pin assignments are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | MIC-IN     |
| 2   | GND        |
| 3   | LINE-L     |
| 4   | GND        |
| 5   | SPK-L      |
| 6   | MIC-VDD    |
| 7   | GND        |
| 8   | LINE-R     |
| 9   | GND        |
| 10  | SPK-R      |



# 2-28. CD AUDIO-IN CONNECTOR

**CD\_IN1 :** CD Audio-in Connector The pin assignments are as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | CD_L       |
| 2   | GND_CD     |
| 3   | GND_CD     |
| 4   | CD_R       |
| 5   | NC         |

|   | <u>۔</u> | יר |  | 1 | 1 |
|---|----------|----|--|---|---|
| 1 | $\Box$   |    |  |   | 5 |

# 2-29. CLEAR CMOS SELECTION

JP8 : Clear CMOS Selection

The selections are as follows:

| FUNCTION   | JUMPER SETTING<br>(pin closed) | JUMPER ILLUSTRATION |
|------------|--------------------------------|---------------------|
| Normal     | 1-2                            | JP8                 |
| Clear CMOS | 2-3                            | JP8                 |

\*\*\* Manufacturing Default - Normal.

# 2-30. MEMORY INSTALLATION

The WLM-6581-06 Embedded Computer supports one SODIMM bank.

# DRAM BANK CONFIGURATION

| DIMM 1 | TOTAL MEMORY |
|--------|--------------|
| 32MB   | 32MB         |
| 64MB   | 64MB         |
| 128MB  | 128MB        |
| 256MB  | 256MB        |
| 512MB  | 512MB        |

# 2-31. TV-OUT CONNECTOR

JP1 : TV-Out Connector

The pin assignment is as follows:

| PIN | ASSIGNMENT |
|-----|------------|
| 1   | LUMA       |
| 2   | DACA       |
| 3   | GND        |
| 4   | GND        |
| 5   | CHROMA     |





# **3-1. INTRODUCTION**

Enclosed with our WLM-6581-06 package is our driver utility, which may comes in a form of a CD ROM disc or floppy diskettes. For CD ROM disc user, you will only need some of the files

| Torrowing chart.             |                              |
|------------------------------|------------------------------|
| Filename                     | Purpose                      |
| (Assume that CD ROM drive is |                              |
| D:)                          |                              |
| D:\Utility\                  | VIA 4in1 Service Pack Driver |
| ***Install this software     | Utility                      |
| first!                       |                              |
| D:\VGA\                      | For VGA driver installation  |
| D:\Flash\                    | For BIOS update              |
| D:\LAN\                      | Realtek RTL8110S             |
|                              | For LAN Driver installation  |
| D:\SOUND\                    | For Sound Driver             |
|                              | installation                 |

contained in the CD ROM disc, please kindly refer to the following chart:

# **3-2. VIA 4IN1 SERVICE PACK DRIVER**

3-2-1. Introduction

The 4-in-1 drivers are a collection of periodically updated drivers that provide enhanced VIA chipset to support under Microsoft Windows. This drivers should be installed after the OS is fully installed, to improve performance, fix issues, and minimize any incompatibilities.

The VIA 4 In 1 driver includes four system drivers to improve the performance and maintain the stability of systems using VIA chipsets. These four drivers are: VIA Registry (INF) Driver, VIA AGP VxD driver, VIA ATAPI Vendor Support Driver and VIA PCI IRQ Miniport Driver



# **3-3. VGA DRIVER UTILITY**

The VGA interface embedded with our WLM-6581-06 can support a wide range of display mode, such as SVGA, STN, TFT .....etc. You can display CRT and LVDS simultaneously with the same mode.



# **3-4. FLASH BIOS UPDATE**

3-4-1. System BIOS Update:

Users of WLM-6581-06 can use the program "Awdflash.exe" contained in the Utility Disk for system BIOS and VGA BIOS update.

# 3-4-2. To update VGA BIOS for LCD Flat Panel Display:

As WLM-6581-06 user, you have to update the VGA BIOS for your specific LCD flat panel you are going to use. For doing this, you need two files. One is the "Awdflash.exe" file and the other is the VGA BIOS for LCD panel display. Both file must be provided by the vendor or manufacturer. When you get these two files ready, follow the following steps for updating your VGA BIOS:

- 1. Install "Awdflash.exe" from Utility Disk to Drive C.
- 2. Insert the VGA BIOS file you have obtained from the vendor.
- 3. Type the path to Awdflash.exe and execute the VGA BIOS update with file C30bxxxx.bin
- C:\UTIL\AWDFLASH>AWDFLASH C30bxxxx.bin 4. The screen will display the table below:



If you want to save up the original BIOS, enter "Y" and press < Enter >. If you choose "N", the following table will appear on screen.

43

| FLASH MEMORY WRITER v8.XX<br>(C) Award Software 2000 All Rights Reserved  |
|---|
| For 8604-686B-6A6LLP69C-0 DATE: 04/23/2001<br>Flash Type - MXIC 29F002(N)T /5V<br>File Name to Program: C30bxxxx.bin<br>Checksum: XXXXX |
| Error Message : Are You Sure To Program (Y/N)   |

Select "Y", and the BIOS will be renewed. When you are refreshing the BIOS, do not turn off or reset the system, or you will damage the BIOS. After you have completed all the programming, the screen displays the table below:

| FLASH MEMORY WRITER v8.XX<br>(C) Award Software 2000 All Rights Reserved  |  |
|---|--|
| For 8604-686B-6A6LLP69C-0 DATE: 04/23/2001<br>Flash Type - MXIC 29F002(N)T /5V<br>File Name to Program: C30bxxxx.bin<br>Checksum: XXXXX |  |
| Reset System or Power off to accomplish update process!   |  |
| F1: Reset F10: Exit   |  |

Please reset or power off the system, and then the Flash BIOS is fully implemented.

# **3-5. LAN DRIVER UTILITY**

# 3-5-1. Introduction

WLM-6581-06 is enhanced with LAN function that can support various network adapters. Installation programs for LAN drivers are listed as follows:



# 3-5-2. Installation Procedures of LAN Driver

# 1. Install LAN Driver to Windows 98/2000/XP

Executing Windows 98/Windows 2000/Windows XP, it will autodetect your system configuration and find the adapter hardware.

- (1) Ask you to select which driver you want to install, select "Driver from disk provided by hardware manufacturer".
- (2) Insert the Realtek RTL8100S driver disk into the drive A or CD drive and specify the setup file pathname, ex: A:\.
- (3) Win 98/ Win 2000 will appear some messages to insert Windows 98/Win2000 system disk to complete setup step.
- (4) Windows 98/Windows 2000 will finish the other installation procedure automatically, and then restart the system.

- 2. Install LAN Driver to Windows NT 3.0/4.0
  - (1) In the Main group of NT, select the "Control Panel" icon.
  - (2) In the Control Panel window, choose the "Network" icon.
  - (3) In the Network Settings dialog box, choose the "Add adapter" button. The Add Network Adapter dialog box appears.
  - (4) In the list of network cards, select "<other> Requires disk from manufacturer", and then press <Enter> button.
  - (5) Insert the LAN driver utility, and enter the filename (ex. A:\ pathname) where the setup file OEMSETUP.INF is located, and then choose OK button.
  - (6) The screen will appear "Select Line Speed" dialog box, which is provided by R8139n5.SYS driver. The default value is "auto" so that the line speed can be auto detected as 10MB or 100MB, while the R8139n5.SYS is loading.
  - (7) The screen will appear "Input Ethernet ID" dialog box, which is provided by R8139n5.SYS driver. This option is only required when you have more than one RTL8100B PCI Fast Ethernet adapters on this computer. Select "SKIP" if only one adapter is installed on this computer.
  - (8) "Bus Location" displayed in next screen. Your machine contains more than one hardware bus, please select the Bus Type and Bus number on which your network adapter card is installed.
  - (9) NT will then perform the binding process. If any additional network software options were installed, you may be prompted for specific information for these packages.
  - (10) Re-starting your system you will acquire network service.
- Dote: For Installing Multiple LAN Adapters:

Enter Windows NT and follow above setup procedure step 2, in the "Network Settings" dialog box, choose the "Configure..." button. The "Input Ethernet ID" dialog box appears and input adapter's Ethernet ID. Last step to select OK and close NETWORK SETUP. Select SKIP if only one adapter is installed on this computer.

# For more information on installation procedure, please refer to TXT directory found on LAN DRIVER UTILITY.

# **3-6. SOUND DRIVER UTILITY**

# 3-6-1. Introduction

The sound function enhanced in this system is fully compatible with Windows 9x/98SE/ME, Windows NT, DOS, OS2, Linux, and Windows 2000. Below, you will find the content of the Sound driver :



3-6-2. Installation Procedure In Windows NT

- (1) Open "Main" Window in Program Manager.
- (2) Select "Control Panel" in Main Window, then open it.
- (3) Select "Drivers" in Control Panel. Double Click it to open this window. Then choose "ADD" item to add driver.
- (4) Choose "Unlisted or Updated Driver" on the list. Then press "OK" button.
- (5) Change the "Install Driver" directory to the "VIA Audio Driver directory". Then press "OK" button.
- (6) If it correct, you will see a pop window, which shows "VIA PCI Audio Controller". Press "OK" button to process installing.
- (7) Restart the computer.



# AWARD BIOS SETUP

This chapter shows how to set up the Award BIOS. Section includes:

- Introduction
- Entering Setup
- The Standard CMOS Features
- The Advanced BIOS Features
- The Advanced Chipset Features
- Integrated Peripherals
- Power Management Setup
- PNP/PCI Configuration
- PC Health Status
- Frequency/Voltage Control
- Load Fail-Safe Defaults
- Load Optimized Defaults
- Password Setting
- Save and Exit Setup
- Exit Without Saving

# **4-1. INTRODUCTION**

This chapter will show you the function of the BIOS in managing the features of your system. The WLM-6581-06 is equipped with the BIOS for system chipset from Award Software Inc. This page briefly explains the function of the BIOS in managing the special features of your system. The following pages describe how to use the BIOS for system chipset Setup menu.

Your application programs (such as word processing, spreadsheets, and games) rely on an operating system such as DOS or OS/2 to manage such things as keyboard, monitor, disk drives, and memory.

The operating system relies on the BIOS (Basic Input and Output system), a program stored on a ROM (Read-only Memory) chip, to initialize and configure your computer's hardware. As the interface between the hardware and the operating system, the BIOS enables you to make basic changes to your system's hardware without having to write a new operating system.

The following diagram illustrates the interlocking relationships between the system hardware, BIOS, operating system, and application program:



49

# **4-2. ENTERING SETUP**

When the system is powered on, the BIOS will enter the Power-On Self Test (POST) routines and the following message will appear on the lower screen: PRESS <DEL> TO ENTER SETUP, ESC TO SKIP MEMORY TEST

As long as this message is present on the screen you may press the <Del> key (the one that shares the decimal point at the bottom of the number keypad) to access the Setup program. In a moment, the main menu of the Award SETUP program will appear on the screen:

| ► Standard CMOS Features              | ► Frequency/Voltage Control                                  |  |
|---------------------------------------|--|--|
| ► Advanced BIOS Features              | Load Fail-Safe Defaults                                      |  |
| ► Advanced Chipset Features           | Load Optimized Defaults                                      |  |
| ► Integrated Peripherals              | Set Password   |  |
| ► Power Management Setup              | Save & Exit Setup  |  |
| ► PnP/PCI Configurations              | Exit Without Saving  |  |
| ► PC Health Status                    |  |  |
| E. O.Y                                |  |  |
| ESC : Quit<br>E10 · Save & Exit Setup | $\downarrow \downarrow \rightarrow \leftarrow$ : Select Item |  |
| FIO. Save & EXIL Setup                |  |  |
| Time, Date, Hard Disk Type            |  |  |

Phoenix - AwardBIOS CMOS Setup Utility

You may use the cursor the up/down keys to highlight the individual menu items. As you highlight each item, a brief description of the highlighted selection will appear at the bottom of the screen.

Setup program initial screen

# **4-3. THE STANDARD CMOS FEATURES**

Highlight the "STANDARD CMOS FEATURES" and press the <ENTER> key and the screen will display the following table:

| Date (mm:dd:yy)<br>Time (hh:mm:ss)                               | Sun Feb 10 2002<br>22 : 20 : 6 | Item Help                |
|--|--------------------------------|--------------------------|
|  |                                | Menu Level 🕨             |
| <ul> <li>IDE Primary Master</li> </ul>                           | [None]                         |                          |
| <ul> <li>IDE Primary Slave</li> </ul>                            | [None]                         | Change the day, month,   |
| <ul> <li>IDE Secondary Master</li> </ul>                         | [None]                         | year and century         |
| ► IDE Secondary Slave  | [None]                         |                          |
| Drive A  | [1 44M 3 5 in ]                |                          |
| Drive B  | [None]                         |                          |
|  | []                             |                          |
| Video  | [EGA/VGA]                      |                          |
| Halt On  | [All, But Keyboard]            |                          |
| Base Memory  | 640K                           |                          |
| Extended Memory  | 506880K                        |                          |
| Total Memory   | 507904K                        |                          |
|  |                                |                          |
| $\uparrow \downarrow \rightarrow \leftarrow$ :Move Enter: Select | +/-/PU/PD:Value F10:Save H     | ESC:Exit F1:General Help |
| F5: Previous Values  | F6:Fail-Safe Defaults F7:      | Optimized Defaults       |
|  | CINC C C                       |                          |

| Phoenix - AwardBIOS CMOS Setup Utility |
|--|
| Standard CMOS Features                 |

**CMOS Setup screen** 

In the above Setup Menu, use the arrow keys to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item.

# Date:

< Month >, < Date > and <Year >. Ranges for each value are in the CMOS Setup Screen, and the week-day will skip automatically.

# Time:

< Hour >, < Minute >, and < Second >. Use 24 hour clock format, i.e., for PM numbers, add 12 to the hour. For example: 4: 30 P.M. You should enter the time as 16:30:00.

# IDE Primary Master / Slave:

# IDE Secondary Master / Slave:

The BIOS can automatically detect the specifications and optimal operating mode of almost all IDE hard drives. When you select type AUTO for a hard drive, the BIOS detect its specifications during POST, every time system boots.

If you do not want to select drive type AUTO, other methods of selecting drive type are available:

- 1. Match the specifications of your installed IDE hard drive(s) with the preprogrammed values for hard drive types 1 through 45.
- 2. Select USER and enter values into each drive parameter field.
- 3. Use the IDE HDD AUTO DETECTION function in Setup.

Here is a brief explanation of drive specifications:

Type: The BIOS contains a table of pre-defined drive types. Each defined drive type has a specified number of cylinders, number of heads, write precompensation factor, landing zone, and number of sectors. Drives whose specifications do not accommodate any predefine type are classified as type USER.

- Size: Disk drive capacity (approximate). Note that this size is usually greater than the size of a formatted disk given by a disk-checking program.
- Cyls: number of cylinders.
- Head: number of heads.
- Precomp: write precompensation cylinders.
- Landz: landing zone.
- Sector: number of sectors.
- Mode: Auto, Normal, Large or LBA.

Auto: The BIOS automatically determines the optimal mode.

- Normal: Maximum number of cylinders, heads, sectors supported are 1024, 16 and 63.
- Large: For drives that do not support LBA and have more than 1024 cylinders.
- LBA (Logical Block Addressing): During drive accesses, the IDE controller transforms the data address described by sector, head and cylinder number into a physical block address, significantly improving data transfer rates. For drives greater than 1024 cylinders.

# **DRIVE A AND DRIVE B:**

Select the type of floppy disk drive installed in your system. The available options are 360KB 5.25in, 1.2KB 5.25in, 720KB 3.5in, 1.44MB 3.5in, 2.88MB 3.5in and None.

# VIDEO:

This category selects the type of video adapter used for the primary system monitor. Although secondary monitors are supported, you do not have to select the type in Setup. Available Options are as follows:

| 21      |   |
|---------|---|
| EGA/VGA | Enhanced Graphics Adapter/Video Graphics Array.<br>For EGA, VGA, SEGA, SVGA or PGA monitor<br>adapters. |
| CGA 40  | Color Graphics Adapter, power up in 40 column mode.   |
| CGA 80  | Color Graphics Adapter, power up in 80 column mode.   |
| MONO    | Monochrome adapter, includes high resolution  |
|         | monochrome adapters.  |

# HALT ON:

This category allows user to choose whether the computer will stop if an error is detected during power up. Available options are "All errors", "No errors", "All, But keyboard", "All, But Diskette", and "All But Disk/Key".

# **BASE MEMORY:**

Displays the amount of conventional memory detected during boot up.

# **EXTENDED MEMORY:**

Displays the amount of extended memory detected during boot up.

# TOTAL MEMORY:

Displays the total memory available in the system.

| HARD | DISK ATTR | RIBUTES | <b>;</b> |       |      |          |
|------|-----------|---------|----------|-------|------|----------|
| Type | Cylinders | Heads   | V-P comp | LZone | Sect | Capacity |
| 1    | 306       | 4       | 128      | 305   | 17   | 10       |
| 2    | 615       | 4       | 300      | 615   | 17   | 20       |
| 3    | 615       | 6       | 300      | 615   | 17   | 30       |
| 4    | 940       | 8       | 512      | 940   | 17   | 62       |
| 5    | 940       | 6       | 512      | 940   | 17   | 46       |
| 6    | 615       | 4       | 65535    | 615   | 17   | 20       |
| 7    | 642       | 8       | 256      | 511   | 17   | 30       |
| 8    | 733       | 5       | 65535    | 733   | 17   | 30       |
| 9    | 900       | 15      | 65535    | 901   | 17   | 112      |
| 10   | 820       | 3       | 65535    | 820   | 17   | 20       |
| 11   | 855       | 5       | 65535    | 855   | 17   | 35       |
| 12   | 855       | 7       | 65535    | 855   | 17   | 49       |
| 13   | 306       | 8       | 128      | 319   | 17   | 20       |
| 14   | 733       | 7       | 65535    | 733   | 17   | 42       |
| 15   | 000       | 0       | 0000     | 000   | 00   | 00       |
| 16   | 612       | 4       | 0000     | 663   | 17   | 20       |
| 17   | 977       | 5       | 300      | 977   | 17   | 40       |
| 18   | 977       | 7       | 65535    | 977   | 17   | 56       |
| 19   | 1024      | 7       | 512      | 1023  | 17   | 59       |
| 20   | 733       | 5       | 300      | 732   | 17   | 30       |
| 21   | 733       | 7       | 300      | 732   | 17   | 42       |
| 22   | 733       | 5       | 300      | 733   | 17   | 30       |
| 23   | 306       | 4       | 0000     | 336   | 17   | 10       |
| 24   | 977       | 5       | 65535    | 976   | 17   | 40       |
| 25   | 1024      | 9       | 65535    | 1023  | 17   | 76       |
| 26   | 1224      | 7       | 65535    | 1223  | 17   | 71       |
| 27   | 1224      | 11      | 65535    | 1223  | 17   | 111      |
| 28   | 1224      | 15      | 65535    | 1223  | 17   | 152      |
| 29   | 1024      | 8       | 65535    | 1023  | 17   | 68       |
| 30   | 1024      | 11      | 65535    | 1023  | 17   | 93       |
| 31   | 918       | 11      | 65535    | 1023  | 17   | 83       |
| 32   | 925       | 9       | 65535    | 926   | 17   | 69       |
| 33   | 1024      | 10      | 65535    | 1023  | 17   | 85       |
| 34   | 1024      | 12      | 65535    | 1023  | 17   | 102      |
| 35   | 1024      | 13      | 65535    | 1023  | 17   | 110      |
| 36   | 1024      | 14      | 65535    | 1023  | 17   | 119      |
| 37   | 1024      | 2       | 65535    | 1023  | 17   | 17       |
| 38   | 1024      | 16      | 65535    | 1023  | 17   | 136      |
| 39   | 918       | 15      | 65535    | 1023  | 17   | 114      |
| 40   | 820       | 6       | 65535    | 820   | 17   | 40       |
| 41   | 1024      | 5       | 65535    | 1023  | 17   | 42       |
| 42   | 1024      | 5       | 65535    | 1023  | 26   | 65       |
| 43   | 809       | 6       | 65535    | 852   | 17   | 40       |
| 44   | 809       | 6       | 65535    | 852   | 26   | 61       |
| 45   | 776       | 8       | 65335    | 775   | 33   | 100      |
| 47   |           |         | AUTO     |       |      |          |

| Award | Hard | Disk | Typ | )e T | [abl | e |
|-------|------|------|-----|------|------|---|
|-------|------|------|-----|------|------|---|

# 4-4. THE ADVANCED BIOS FEATURES

Choose the "ADVANCED BIOS FEATURES" in the main menu, the screen shown as below.

| Virus Warning  | [Disabled] | Item Help                  |  |  |
|--|------------|----------------------------|--|--|
| CPU Internal Cache   | [Enabled]  | item inerp                 |  |  |
| External Cache   | [Enabled]  |                            |  |  |
| CPU L2 Cache ECC Checking  | [Enabled]  | Menu Level                 |  |  |
| Quick Power On Self Test   | [Enabled]  |                            |  |  |
| USB Flash Disk Type  | [Floopy]   |                            |  |  |
| First Boot Device  | [Floppy]   | Allows you to choose       |  |  |
| Second Boot Device   | [HDD-0]    | the VIRUS warning          |  |  |
| Third Boot Device  | [LS120]    | feature for IDE Hard       |  |  |
| Boot Other Device  | [Enabled]  | Disk boot sector           |  |  |
| Swap Floppy Drive  | [Disabled] | protection. If this        |  |  |
| Boot Up Floppy Seek  | [Disabled] | function is enabled and    |  |  |
| Boot Up NumLock Status   | [On]       | someone attempt to         |  |  |
| Gate A20 Option  | [Fast]     | write data into this area, |  |  |
| Typematic Rate Setting   | [Disabled] | BIOS will show a           |  |  |
| x Typematic Rate (Chars/Sec)   | 6          | warning message on         |  |  |
| x Typematic Delay (Msec)   | 250        | screen and alarm beep      |  |  |
| Security Option  | [Setup]    |                            |  |  |
| OS Select for DRAM > 64MB  | [Non-OS2]  |                            |  |  |
| Video BIOS Shadow  | [Enabled]  |                            |  |  |
| C8000-CBFFF Shadow   | [Disabled] |                            |  |  |
| CC000-CFFFF Shadow   | [Disabled] |                            |  |  |
| D0000-D3FFF Shadow   | [Disabled] |                            |  |  |
| D4000-D7FFF Shadow   | [Disabled] |                            |  |  |
| D8000-DBFFF Shadow   | [Disabled] |                            |  |  |
| DC000-DFFFF Shadow   | [Disabled] |                            |  |  |
| Small Logo (EPA) Show  | [Enabled]  |                            |  |  |
| ↑↓→←:Move Enter: Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help<br>F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults |            |                            |  |  |

Phoenix - AwardBIOS CMOS Setup Utility Advanced BIOS Features

# **BIOS Features Setup Menu**

The "BIOS FEATURES SETUP" allow you to configure your system for basic operation. The user can select the system's default speed, boot-up sequence, keyboard operation, shadowing and security.

A brief introduction of each setting in the BIOS FEATURES SETUP program is given below.

# **VIRUS WARNING :**

This item allows you to choose the Virus Warning feature for IDE Hard Disk boot sector protection. If this function is enabled and someone attempt to write data into this area, BIOS will show a warning message on screen and alarm beep.

#### CPU INTERNAL CACHE/EXTERNAL CACHE :

These two categories speed up memory access. However, it depends on CPU/chipset design.

#### **CPU L2 CACHE ECC CHECKING :**

This item allows you to enable or disable CPU L2 Cache ECC checking.

# QUICK POWER ON SELF-TEST:

This item allows you to speed up Power On Self Test (POST) after power-up the computer. When enabled, the BIOS will shorten or skip some check items during POST.

# **USB FLASH TYPE**

Select USB Flash device type. If select Floppy then boot device has to select USB\_ZIP. If select HDD, then boot device has to select USB\_HDD.

# FIRST/SECOND/THIRD/OTHER BOOT DEVICE:

The BIOS attempt to load the operating system from the devices in the sequence selected in these items.

# SWAP FLOOPY DRIVE:

This field is effective only in systems with two floppy drives. Selecting Enabled assigns physical drive B to logical drive A, and physical drive A to logical drive B.

# **BOOT UP FLOPPY SEEK:**

You may enable / disable this item to define whether the system will look for a floppy disk drive to boot at power-on, or proceed directly to the hard disk drive.

# **BOOT UP NUMLOCK STATUS:**

Select power on state for NumLock.

# GATE A20 OPTION:

This entry allows you to select how the gate A20 is handled. When Normal was set, a pin in the keyboard controller controls Gate A20. And when Fast was set, the chipset controls Gate A20.

# **TYPEMATIC RATE SETTING:**

Enable this item if you wish to be able to configure the characteristics of your keyboard. Typematic refers to the way in which characters are entered repeatedly if a key is held down. For example, if you press and hold down the "A" key, the letter "a" will repeatedly appear on your screen on your screen until you release the key. When enabled, the typematic rate and typematic delay can be selected.

# **TYPEMATIC RATE (CHARS/SEC):**

This item sets the number of times a second to repeat a key stroke when you hold the key down.

# **TYPEMATIC DELAY (MSEC):**

The item sets the delay time after the key is held down before it begins to repeat the keystroke.

# **SECURITY OPTION:**

This category allows you to limit access to the system and Setup, or just to Setup.

| System | The system will not boot and access to Setup will be<br>denied if the correct password is not entered at the |
|--------|--|
|        | prompt.  |
| Setup  | The system will boot, but access to Setup will be<br>denied if the correct password is not entered at the    |
|        | prompt.  |

# OS SELECT FOR DRAM >64MB :

Select the operating system that is running with greater than 64MB or RAM on the system. You may choose OS2 or Non-OS2.

# **VIDEO BIOS SHADOW :**

Determines whether video BIOS will be copied to RAM. However, it is optional depending on chipset design. Video Shadow will increase the video speed.

# C8000-CBFFF SHADOW ~ DC000-DFFFF SHADOW:

These categories determine whether option ROMs will be copied to RAM. An example of such option ROM would be support of on-board SCSI.

# **4-5. ADVANCED CHIPSET FEATURES**

Choose the "ADVANCED CHIPSET FEATURES" from the main menu, the screen shown as below.

|    |   | <u>^</u>                      |                      |
|----|---|-------------------------------|----------------------|
| x  | DRAM Timing by SPD<br>DRAM Clock                        | [Enabled]<br>Host CLK         | Item Help            |
|    | SDRAM Cycele Longth                                     | 2                             |                      |
| х  | SDRAM Cycle Length                                      | 5                             | Menu Level 🕨         |
| х  | Bank Interleave   | Disabled                      |                      |
|    | Memory Hole   | [Disabled]                    |                      |
|    | P2C/C2P Concurrency                                     | [Enabled]                     |                      |
|    | System BIOS Cacheable                                   | [Disabled]                    |                      |
|    | Video RAM Cacheable                                     | [Disabled]                    |                      |
|    | Frame Buffer Size                                       | [16M]                         |                      |
|    | Select Display Device                                   | [Auto]                        |                      |
|    | Panel Type  | [LVDS 1024 x 768 18bit]       |                      |
|    | OnChip USB  | [Enabled]                     |                      |
|    | USB Keyboard Support                                    | [Disabled]                    |                      |
|    | OnChip Sound  | [Auto]                        |                      |
|    | CPU to PCI Write Buffer                                 | [Enabled]                     |                      |
|    | PCI Dynamic Bursting                                    | [Enabled]                     |                      |
|    | PCI Master 0 WS Write                                   | [Enabled]                     |                      |
|    | PCI Delay Transaction                                   | [Disabled]                    |                      |
|    | PCI#2 Access #1 Retry                                   | [Enabled]                     |                      |
|    | •   |                               |                      |
| ↑. | $\downarrow \rightarrow \leftarrow$ :Move Enter: Select | +/-/PU/PD:Value F10:Save ESC: | Exit F1:General Help |
|    | F5: Previous Values                                     | F6:Fail-Safe Defaults F7:Opt  | mized Defaults       |
|    |   |                               |                      |

# Phoenix - AwardBIOS CMOS Setup Utility Advanced Chipset Features

**Chipset Features Setup Screen** 

The parameter allows you to configure the system based on the specific features of the installed chipset. The chipset manages bus speed and access to system memory resources, such as DRAM and the external cache.

It also coordinates communications between conventional ISA bus and the PCI bus. It must be stated that these items should never need to be altered. The default settings have been chosen because they provide the best opera-ting conditions for the system. The only time you might consider making any changes would be if you discovered that data was being lost while using your system.

# DRAM TIMING BY SPD:

User can control DRAM Timing by SPD.

# DRAM CLOCK:

This item allows you to control the DRAM speed.

# SDRAM CYCLE LENGTH:

When synchronous DRAM is installed, the number of clock cycles of CAS latency depends on the DRAM timing. Do not reset this field from the default value specified by the system designer.

#### **BANK INTERLEAVE:**

This item allows you to set how many banks of SDRAM support in your mainboard.

# **MEMORY HOLE:**

In order to improve performance, certain space in memory is reserved for ISA cards. This memory must be mapped into the memory space below 16MB.

#### P2C/C2P CONCURRENCY:

This item allows you to enable/disable the PCI to CPU, CPU to PCI concurrency.

## SYSTEM BIOS CACHEABLE:

This item allows you to enable caching of the system BIOS ROM at F0000h-FFFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result.

# VIDEO RAM CACHEABLE:

Select Enabled allows caching of the video RAM, resulting in better system performance. However, if any program writes to this memory area, a system error may result.

#### FRAME BUFFER SIZE:

This item allows you to control the VGA frame buffer size.

# ONCHIP USB:

This should be enabled if your system has a USB installed on the system board and you want to use it. Even when so equipped, if you add a higher performance controller, you will need to disable this feature.

# **USB KEYBOARD SUPPORT:**

Select Enabled if your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard.

# **ONCHIP SOUND:**

This item allows you to control the onboard AC '97 audio.

# **CPU TO PCI WRITE BUFFER:**

When this field is Enabled, writes from the CPU to the PCI bus are buffered, to compensate for the speed differences between the CPU and the PCI bus.

When Disabled, the writes are not buffered and the CPU must wait until the write is complete before starting another write cycle.

#### **PCI DYNAMIC BURSTING:**

When Enabled, every write transaction goes to the write buffer. Burstable transactions then burst on the PCI bus and non-burstable transaction don't.

## PCI MASTER 0 WS WRITE:

When Enabled, writes to the PCI bus are executed with zero wait states.

## **PCI DELAY TRANSACTION:**

The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles. Select Enabled to support compliance with PCI specification version 2.1.

# PCI#2 ACCESS #1 RETRY:

When disabled, PCI#2 will not be disconnected until access finishes. When Enabled, PCI#2 will be disconnected if max retries are attempted without success.

# **4-6. INTEGRATED PERIPHERALS**

Choose "INTEGRATED PERIPHERALS" from the main setup menu, a display will be shown on screen as below:

|            | integrated i empiretais                                 |                              |                       |  |  |
|------------|---|------------------------------|-----------------------|--|--|
|            | On-Chip IDE Channel0                                    | [Enabled]                    | Item Heln             |  |  |
|            | On-Chip IDE Channel1                                    | [Enabled]                    | Item Help             |  |  |
|            | IDE Prefetch Mode                                       | [Enabled]                    |                       |  |  |
|            | Primary Master PIO                                      | [Auto]                       | Menu Level 🕨          |  |  |
|            | Primary Slave PIO                                       | [Auto]                       |                       |  |  |
|            | Secondary Master PIO                                    | [Auto]                       |                       |  |  |
|            | Secondary Slave PIO                                     | [Auto]                       |                       |  |  |
|            | Primary Master UDMA                                     | [Auto]                       |                       |  |  |
|            | Primary Slave UDMA                                      | [Auto]                       |                       |  |  |
|            | Secondary Master UDMA                                   | [Auto]                       |                       |  |  |
|            | Secondary Slave UDMA                                    | [Auto]                       |                       |  |  |
|            | Init Display First                                      | [Onboard VGA]                |                       |  |  |
|            | IDE HDD Block Mode                                      | [Enabled]                    |                       |  |  |
|            | LAN Boot ROM  | [Enabled]                    |                       |  |  |
|            | Onboard FDD Controller                                  | [Enabled]                    |                       |  |  |
|            | Onboard Serial Port 1                                   | [3F8 / IRQ4]                 |                       |  |  |
|            | Onboard Serial Port 2                                   | [2F8 / IRQ3]                 |                       |  |  |
|            | UART 2 Mode   | [Standard]                   |                       |  |  |
| х          | IR Function Duplex                                      | Half                         |                       |  |  |
| х          | TX,RX inverting enable                                  | No, Yes                      |                       |  |  |
|            | Onboard Parallel Port                                   | [378/IRQ7]                   |                       |  |  |
|            | Onboard Parallel Mode                                   | [Normal]                     |                       |  |  |
| х          | ECP Mode Use DMA  | 3                            |                       |  |  |
| х          | Parallel Port EPP Type                                  | EPP1.9                       |                       |  |  |
|            | Onboard Serial Port 3                                   | [3E8]                        |                       |  |  |
|            | Serial Port 3 User IRQ                                  | [IRQ10]                      |                       |  |  |
|            | Onboard Serial Port 4                                   | [2E8]                        |                       |  |  |
|            | Serial Port 4 Use IRQ                                   | [IRQ11]                      |                       |  |  |
|            | IO Channel Check NMI                                    | [Disabled]                   |                       |  |  |
|            |   |                              |                       |  |  |
| $\uparrow$ | $\downarrow \rightarrow \leftarrow$ :Move Enter: Select | +/-/PU/PD:Value F10:Save ESC | :Exit F1:General Help |  |  |
|            | F5: Previous Values                                     | F6:Fail-Safe Defaults F7:Op  | timized Defaults      |  |  |

| Phoenix - AwardBIOS CMOS Setup Utility |
|--|
| Integrated Peripherals                 |

# **Integrated Peripherals Setup Screen**

By moving the cursor to the desired selection and by pressing the  $\langle F1 \rangle$  key, the all options for the desired selection will be displayed for choice.

# ONCHIP IDE CHANNEL 0/1:

The chipset contains a PCI IDE interface with support for two IDE channels. Select Enabled to activate the primary IDE interface. Select Disabled to deactivate this interface.

# **IDE PREFETCH MODE:**

The onboard IDE drive interfaces supports IDE pre-fetching for faster drive accesses. If you install a primary and or secondary add-in IDE interface, set this field to *Disabled* if the interface does not support pre-fetching.

# PRIMARY MASTER/SLAVE PIO: SECONDARY MASTER/SLAVE PIO:

The four IDE PIO fields allow you to set a PIO mode (0-4) for each of the four IDE devices that the onboard IDE interface supports. Modes 0 through 4 provide successively increased performance. In Auto mode, the system automatically determines the best mode for each device.

# PRIMARY MASTER/SLAVE UDMA: SECONDARY MASTER/SLAVE UDMA:

Ultra DMA/33 implementation is possible only if your IDE hard drive supports it and the operating environment includes a DMA driver (Windows 95 OSR2 or a third-party IDE bus master driver). If you hard drive and your system software both support Ultra DMA/33, select Auto to enable BIOS support.

#### **INIT DISPLAY FIRST:**

This item allows you to decide to active whether PCI Slot or on-chip VGA first. The choices are PCI Slot and Onboard.

# **IDE HDD BLOCK MODE:**

Block mode is also called block transfer, multiple commands, or multiple sector read/write. If your IDE hard drive supports block mode (most new drives do), select Enabled for automatic detection of the optimal number of block read/writes per sector the drive can support.

63

# LAN BOOT ROM:

This item control Lan Boot Rom message to be showed or not.

### **ONBOARD FDD CONTROLLER:**

Select Enabled if the system has a floppy disk controller (FDC) installed on the system board and you wish to use it. If you install and-in FDC or the system has no floppy drive, select Disabled.

ONBOARD SERIAL PORT 1: ONBOARD SERIAL PORT 2: ONBOARD SERIAL PORT 3: ONBOARD SERIAL PORT 4:

Select an address and corresponding interrupt for the 1<sup>st</sup>, 2<sup>nd</sup>, 3<sup>rd</sup> and forth serial ports.

#### UART 2 MODE:

This item allows you to select which mode for the Onboard Serial Port 2.

#### **IR FUNCTION DUPLEX:**

This item allows you to select the IR half/full duplex function.

# TX, RX INVERTING ENABLE:

This item allows you to enable TX, RX inverting which depends on different H/W requirement. This field is not recommended to change its default setting for avoiding any error in your system.

# **ONBOARD PARALLEL PORT:**

This item allows you to determine access onboard parallel port controller with which I/O address.

# ONBOARD PARALLEL MODE:

Select an operating mode for the onboard (printer) port. Select *Normal* unless you are certain your hardware and software both support one of the other available modes.

# ECP MODE USE DMA:

Select a DMA channel for the parallel port for use during ECP mode.

# PARALLEL PORT EPP TYPE:

Select EPP port type 1.7 or 1.9 as required by your parallel peripheral.

# SERIAL PORT 3 USE IRQ:

# **SERIAL PORT 4 USE IRQ:**

Select an address and corresponding interrupt for the 3<sup>rd</sup> and forth serial ports.

# **IO CHANNEL CHECK NMI:**

This field enables or disables IO channel check NMI. Before selecting this function, the user should check first that NMI function is enabled as described in chapter 2

# **4-7. POWER MANAGEMENT SETUP**

Choose "POWER MANAGEMENT SETUP" option on the main menu, a display will be shown on screen as below :

| ACPI function<br>► Power Management  | [Disabled]<br>[Press Enter]   | Item Help                              |
|--|---|--|
| PM Control by APM<br>Video Off Option<br>Video Off Method<br>MODEM Use IRQ<br>Soft-off by PWRBTN<br>State After Power Failur<br>► Wake Up Events | [Yes]<br>[Suspend -> Off]<br>[V/H SYNC+Blank]<br>[3]<br>[Instant-off]<br>[Off]<br>[Press Enter] | Menu Level ►                           |
| $ \begin{array}{c} \uparrow \downarrow \rightarrow \leftarrow: Move  Enter: \ Select \\ F5: \ Previous \ Values \end{array} $                    | +/-/PU/PD:Value F10:Save ESC:1<br>F6:Fail-Safe Defaults F7:Opti                                 | Exit F1:General Help<br>mized Defaults |

Phoenix - AwardBIOS CMOS Setup Utility Power Management Setup

**Power Management Setup Screen** 

The "Power Management Setup" allows the user to configure the system to the most effectively save energy while operating in a manner consistent with your own style of computer use.

65

# **ACPI FUNCTION:**

Users are allowed to enable or disable the Advanced Configuration and Power Management (ACPI).

# **POWER MANAGEMENT:**

This item allows the user to select the Power Management Mode.

# PM CONTROL BY APM:

If Advanced Power Management (APM) is installed on your system, selecting Yes gives better power savings.

# VIDEO OFF OPTION:

This category determines the power-saving modes during which the monitor goes blank:

| ALWAYS ON                   | Monitor remains on during power-saving modes. |  |  |
|-----------------------------|---|--|--|
| SUSPEND $\rightarrow$ OFF   | Monitor blanked when system enters Suspend    |  |  |
|                             | mode.   |  |  |
| SUSP,STBY $\rightarrow$ OFF | Monitor blanked when system enters either     |  |  |
|                             | Suspend or Standby mode.                      |  |  |
| ALL MODES $\rightarrow$ OFF | Monitor blanked when system enters any power  |  |  |
|                             | saving mode.                                  |  |  |

# VIDEO OFF METHOD:

This category determines the manner in which the monitor is blanked.

| V/H SYNC+BLANK | This selection will cause the system to turn off   |  |
|----------------|--|--|
|                | the vertical & horizontal synchronization po       |  |
|                | and writes blanks to video buffer.                 |  |
| BLANK SCREEN   | This selection only writes blanks to video buffer. |  |
| DPMS SUPPORT   | Initial display power management signaling.        |  |

# MODEM USE IRQ:

This item enable you to name the interrupt request (IRQ) line assigned to the modem (if any) on your system. Activity of the selected IRQ always awakens the system.

# SOFT-OFF BY PWRBTN:

Pressing the power button for more than 4 seconds forces the system to enter the Soft-Off state when the system has "hung". The choices are Delay 4 Sec and Instant-Off.

# STATE AFTER POWER FAILURE:

This field lets you determine the state that your PC returns to after a power failure. If set to Off, the PC will not boot after a power failure. IF set to On, the PC will restart after a power failure.

#### WAKE UP EVENTS:

Wake up events are I/O events whose occurrence can prevent the system from entering a power saving mode or can awaken the system from such a mode. In effect, the system remains alert for anything that occurs to a device, which is configured as ON, even when the system is in a power down mode.

# VGA:

When Enabled, you can set the VGA awakens the system.

# LPT & COM:

When ON of LPT & COM, any activity from one of the listed system peripheral devices or IRQs wakes up the system.

#### HDD & FDD:

When ON of HDD & FDD, any activity from one of the listed system peripheral devices wakes up the system.

# PCI MASTER:

When *ON of PCI Master*, any activity from one of the listed system peripheral devices wakes up the system.

#### WAKE UP ON LAN/RING:

This category allows you to wake up the system from LAN from remote host. And it also can be awaken from an input signal on serial Ring Indicator (RI) line (incoming call on the modem).

# **RTC ALARM RESUME:**

When Enabled, you can set the date and the time at which the RTC alarm awakens the system from Suspend mode.

#### **PRIMARY INTR:**

When set to Off, IRQ Activity Monitoring is set to BIOS default. When set to On, user may select the desired setting.

# **4-8. PNP/PCI CONFIGURATION**

Choose "PNP/PCI CONFIGURATION" from the main menu, a display will be shown on screen as below:

| _      |  |  |  |  |  |
|--------|--|--|--|--|--|
|        | PNP OS Installed<br>Reset Configuration Data   | [Yes]<br>[Disabled]                        | Item Help  |  |  |
| x<br>x | Resources Controlled By<br>IRQ Resources<br>DMA Resources  | [Auto(ESCD)]<br>Press Enter<br>Press Enter | Menu Level ►<br>Select Yes if you are  |  |  |
|        | PCI/VGA Palette Snoop<br>Assign IRQ for VGA<br>Assign IRQ for USB  | [Disabled]<br>[Enabled]<br>[Enabled]       | using a Plug and Play<br>capable operating<br>system Select No if<br>you need the BIOS to<br>configure non-boot<br>devices |  |  |
| 1      | ↑↓→←:Move Enter: Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help<br>F5: Previous Values F6:Fail-Safe Defaults F7:Optimized Defaults |  |  |  |  |

#### Phoenix - AwardBIOS CMOS Setup Utility PnP/PCI Configurations

**PNP/PCI Configuration Setup Screen** 

This section describes how to configure PCI bus system. PCI, also known as Personal Computer Interconnect, is a system, which allows I/O devices to operate at speeds nearing the speed of the CPU itself uses when communicating with its own special components. This section covers technical items, which is strongly recommended for experienced users only.

# **PNP OS INSTALLED:**

This item allows you to determine install PnP OS or not.

# **RESET CONFIGURATION DATA:**

Normally, you leave this field Disabled. Select Enabled to reset Extended System Configuration Data (ESCD) when you exit Setup if you have installed a new add-on and the system configuration has caused such a serious conflict that the operating system cannot boot.

# **RESOURCE CONTROLLED BY:**

The Award Plug and Play Bios can automatically configure all of the booth and Plug and Play-compatible devices. However, this capability means absolutely nothing unless you are using a Plug and Play operating system such as Windows 95. By choosing "manual", you are allowed to configure the *IRQ Resources*, *DMA Resources* and *Memory Resources*. The choices are Auto (ESCD) and Manual.

# **IRQ RESOURCES:**

You may assign each system interrupt a type, depending on the type of device using the interrupt.

#### DMA RESOURCES:

When resources are controlled manually, assign each system DMA channel a type, depending on the type of device using the DMA channel.

# PCI/VGA PALETTE SNOOP:

Leave this field at disabled.

# **ASSIGN IRQ FOR USB:**

Enable or Disable to assign IRQ for USB.

# **ASSIGN IRQ FOR VGA:**

Enable or Disable to assign IRQ for VGA.

# 4-9. PC HEALTH STATUS

Choose "PC HEALTH STATUS" from the main menu, a display will be shown on screen as below:

| CPU Warning Temperatu<br>Current CPU Temp.<br>Current CPU Fan Speed<br>Vcore<br>2.5V<br>3.3V<br>5V<br>12V<br>Shutdown Temperature | ure [Disabled]<br>28°C/ 82°F<br>5957 RPM<br>1.08V<br>2.52V<br>3.35V<br>4.88V<br>12.12V<br>[Disabled] | Item Help<br>Menu Level ►                 |
|---|--|---|
| ↑↓→←:Move Enter: Select<br>F5: Previous Values  | +/-/PU/PD:Value F10:Save ESC<br>F6:Fail-Safe Defaults F7:Op  | Exit F1:General Help:<br>timized Defaults |

Phoenix - AwardBIOS CMOS Setup Utility PC Health Status

PC Health Status Setup Screen

The setup menu allows you to select whether to choose between monitoring or ignoring the hardware monitoring function of your system.

# **CPU WARNING TEMPERATURE:**

In the DOS environment, if the temperature reaches the warning point the buzzer will act.

# **CURRENT CPU TEMPERATURE:**

This item shows you the current CPU temperature.

# **CURRENT SYSTEM FAN SPEED:**

This item shows you the current System FAN speed.

# VCORE:

This item shows you the current system voltage.

# SHUTDOWN TEMPERATURE:

This item allows you to set up the CPU shutdown Temperature. This function is only effective under Windows 98 ACPI mode.

# 4-10. FREQUENCY/VOLTAGE CONTROL

Choose "FREQUENCY/VOLTAGE CONTROL" from the main menu, a display will be shown on screen as below:

|  | riequency control   |  |
|--|---|--|
| VIA Processor Clock Rat<br>Auto Detect DIMM/PCI (<br>Spread Spectrum | io [Default]<br>Clk [Enabled]                             | Item Help  |
|  | [Disabled]  | Menu Level 🕨                                     |
|  |   | This item is for VIA C3<br>CPU Ratio adjustment. |
|  |   |  |
|  |   |  |
|  |   |  |
|  |   |  |
|  |   |  |
|  | +/-/PU/PD:Value F10:Save E5<br>F6:Fail-Safe Defaults F7:C | SC:Exit F1:General Help<br>Dptimized Defaults    |

| Phoenix - AwardBIOS CMOS Setup Utility |
|--|
| Frequency Control                      |

Frequency / Voltage Control Setup Screen

This setup menu allows you to specify your settings for frequency/voltage control.

# VIA PROCESSOR CLOCK RATIO:

This item is for VIA Processor CPU ratio adjustment.

# AUTO DETECT DIMM/PCI CLK:

This item allows you to enable or disable auto detect DIMM/PCI Clock.

# **SPREAD SPECTRUM:**

This item allows you to enable or disable the spread spectrum modulate.

# 4-11. LOAD FAIL-SAFE DEFAULTS

By pressing the <ENTER> key on this item, you get a confirmation dialog box with a message similar to the following:

To use the BIOS default values, change the prompt to "Y" and press the <Enter > key. CMOS is loaded automatically when you power up the system.

# 4-12. LOAD OPTIMIZED DEFAULTS

When you press <Enter> on this category, you get a confirmation dialog box with a message similar to the following:

```
Load Optimized Defaults ( Y/N ) ? N
```

Pressing "Y" loads the default values that are factory setting for optimal performance system operations.
### 4-13. PASSWORD SETTING

User is allowed to set either supervisor or user password, or both of them. The difference is that the supervisor password can enter and change the options of the setup menus while the user password can enter only but do not have the authority to change the options of the setup menus.

# TO SET A PASSWORD

When you select this function, the following message will appear at the center of the screen to assist you in creating a password.



Type the password up to eight characters in length, and press < Enter >. The password typed now will clear any previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press the < Enter > key. You may also press < Esc > to abort the selection and not enter a password.

User should bear in mind that when a password is set, you will be asked to enter the password everything you enter CMOS setup Menu.

# TO DISABLE THE PASSWORD

To disable the password, select this function (do not enter any key when you are prompt to enter a password), and press the <Enter> key and a message will appear at the center of the screen:

PASSWORD DISABLED!!! Press any key to continue...

Press the < Enter > key again and the password will be disabled. Once the password is disabled, you can enter Setup freely.

### 4-14. SAVE & EXIT SETUP

After you have completed adjusting all the settings as required, you must remember to save these setting into the CMOS RAM. To save the settings, select "SAVE & EXIT SETUP" and press <Enter>, a display will be shown as follows:



When you confirm that you wish to save the settings, your system will be automatically restarted and the changes you have made will be implemented. You may always call up the setup program at any time to adjust any of the individual items by pressing the <Del> key during boot up.



## 4-15. EXIT WITHOUT SAVING

If you wish to cancel any changes you have made, you may select the "EXIT WITHOUT SAVING" and the original setting stored in the CMOS will be retained. The screen will be shown as below:



Phoenix - AwardBIOS CMOS Setup Utility

75

# **APPENDIX**



# EXPANSION BUS

This appendix indicates you the pin assignments.

Section includes:

- PC-104 Connector Pin Assignment
- PC-104 Plus Connector Pin Assignment
- Compact Flash Card Connector Pin Assignment
- PCI BUS Pin Assignment

## **PC-104 CONNECTOR PIN ASSIGNMENT**

104AB, 104CD : PC-104 Connector



The PC-104 can support multi-pieces of PC-104 modules. It has two connectors : one (104AB) consists of 64 pin; the other one (104CD) consists of 40 pin, both of them are dual-in-line headers

| PIN | ASSIGNMENT | PIN | ASSIGNMENT | PIN | ASSIGNMENT | PIN | ASSIGNMENT |
|-----|------------|-----|------------|-----|------------|-----|------------|
| A1  | IOCHK      | B1  | GND        | C1  | GND        | D1  | GND        |
| A2  | D7         | B2  | REST       | C2  | SBHE       | D2  | MEMCS16    |
| A3  | D6         | B3  | VCC        | C3  | LA23       | D3  | IOCS16     |
| A4  | D5         | B4  | IRQ9       | C4  | LA22       | D4  | IRQ10      |
| A5  | D4         | B5  | - 5V       | C5  | LA21       | D5  | IRQ11      |
| A6  | D3         | B6  | DRQ2       | C6  | LA20       | D6  | IRQ12      |
| Α7  | D2         | B7  | -12V       | C7  | LA19       | D7  | IRQ15      |
| A8  | D1         | B8  | OWS        | C8  | LA18       | D8  | IRQ14      |
| A9  | DO         | B9  | +12V       | C9  | LA17       | D9  | DACKO      |
| A10 | IOCHRDY    | B10 | GND        | C10 | MEMR       | D10 | DRQO       |
| A11 | AEN        | B11 | SMEMW      | C11 | MEMW       | D11 | DACK5      |
| A12 | A19        | B12 | SMEMR      | C12 | D8         | D12 | DRQ5       |
| A13 | A18        | B13 | IOW        | C13 | D9         | D13 | DACK6      |
| A14 | A17        | B14 | IOR        | C14 | D10        | D14 | DRQ6       |
| A15 | A16        | B15 | DACK3      | C15 | D11        | D15 | DACK7      |
| A16 | A15        | B16 | DRQ3       | C16 | D12        | D16 | DRQ7       |
| A17 | A14        | B17 | DACK1      | C17 | D13        | D17 | VCC        |
| A18 | A13        | B18 | DRQ1       | C18 | D14        | D18 | MASTER     |
| A19 | A12        | B19 | REFRESH    | C19 | D15        | D19 | GND        |
| A20 | A11        | B20 | CLK        | C20 | KEY PIN    | D20 | GND        |
| A21 | A10        | B21 | IRQ7       |     |            |     |            |
| A22 | A9         | B22 | IRQ6       |     |            |     |            |
| A23 | A8         | B23 | IRQ5       |     |            |     |            |
| A24 | A7         | B24 | IRQ4       |     |            |     |            |
| A25 | A6         | B25 | IRQ3       |     |            |     |            |
| A26 | A5         | B26 | DACK2      |     |            |     |            |
| A27 | A4         | B27 | TC         |     |            |     |            |
| A28 | A3         | B28 | BALE       |     |            |     |            |
| A29 | A2         | B29 | VCC        |     |            |     |            |
| A30 | A1         | B30 | OSC        |     |            |     |            |
| A31 | AO         | B31 | GND        |     |            |     |            |
| A32 | GND        | B32 | GND        |     |            |     |            |

The pin assignments for connector 104AB & 104CD are as follow:

77

# PC/104 PLUS BUS CONNECTOR PIN ASSIGNMENT

PC/104 Plus Bus connector is divided into four rows. Each row consists of 30 pins. The pin assignments are as followed:

|     | А          | В   |            |     | С          | D   |            |  |
|-----|------------|-----|------------|-----|------------|-----|------------|--|
| PIN | ASSIGNMENT | PIN | ASSIGNMENT | PIN | ASSIGNMENT | PIN | ASSIGNMENT |  |
| A1  | GND        | B1  | Reserved   | C1  | +5V        | D1  | AD00       |  |
| A2  | V/I/O      | B2  | AD02       | C2  | AD01       | D2  | +5V        |  |
| A3  | AD05       | B3  | GND        | C3  | AD04       | D3  | AD03       |  |
| A4  | C/BEO#     | B4  | AD07       | C4  | GND        | D4  | AD06       |  |
| A5  | GND        | B5  | AD09       | C5  | AD08       | D5  | GND        |  |
| A6  | AD11       | B6  | V/I/O      | C6  | AD10       | D6  | M66EN      |  |
| A7  | AD14       | B7  | AD13       | C7  | GND        | D7  | AD12       |  |
| A8  | +3.3V      | B8  | C/BE1#     | C8  | AD15       | D8  | +3.3V      |  |
| A9  | SERR#      | B9  | GND        | C9  | SBO#       | D9  | PAR        |  |
| A10 | GND        | B10 | PERR#      | C10 | +3.3V      | D10 | SDONE      |  |
| A11 | STOP#      | B11 | +3.3V      | C11 | LOCK#      | D11 | GND        |  |
| A12 | +3.3V      | B12 | TRDY#      | C12 | GND        | D12 | DEVSEL     |  |
| A13 | FRAME#     | B13 | GND        | C13 | IRDY#      | D13 | +3.3V      |  |
| A14 | GND        | B14 | AD16       | C14 | +3.3V      | D14 | C/BE2#     |  |
| A15 | AD18       | B15 | +3.3V      | C15 | AD17       | D15 | GND        |  |
| A16 | AD21       | B16 | AD20       | C16 | GND        | D16 | AD19       |  |
| A17 | +3.3V      | B17 | AD23       | C17 | AD22       | D17 | +3.3V      |  |
| A18 | IDSEL0     | B18 | GND        | C18 | IDSEL1     | D18 | IDSEL2     |  |
| A19 | AD24       | B19 | C/BE3#     | C19 | V/I/O      | D19 | IDSEL3     |  |
| A20 | GND        | B20 | AD26       | C20 | AD25       | D20 | GND        |  |
| A21 | AD29       | B21 | +5V        | C21 | AD28       | D21 | AD27       |  |
| A22 | +5V        | B22 | AD30       | C22 | GND        | D22 | AD31       |  |
| A23 | REQ0#      | B23 | GND        | C23 | REQ1#      | D23 | V/I/O      |  |
| A24 | GND        | B24 | REQ2#      | C24 | +5V        | D24 | GNTO#      |  |
| A25 | GNT1#      | B25 | V/I/O      | C25 | GNT2#      | D25 | GND        |  |
| A26 | +5V        | B26 | CLK0       | C26 | GND        | D26 | CLK1       |  |
| A27 | CLK2       | B27 | +5V        | C27 | CLK3       | D27 | GND        |  |
| A28 | GND        | B28 | INTD#      | C28 | +5V        | D28 | RST#       |  |
| A29 | +12V       | B29 | INTA#      | C29 | INTB#      | D29 | INTC#      |  |
| A30 | -12V       | B30 | Reserved   | C30 | Reserved   | D30 | GND        |  |

The pin assignments of Compact Flash Card connector are stated below.

| PIN | Assignment | PIN | Assignment |
|-----|------------|-----|------------|
| 1   | GND        | 26  | -CD1       |
| 2   | D03        | 27  | D111       |
| 3   | D04        | 28  | D121       |
| 4   | D05        | 29  | D131       |
| 5   | D06        | 30  | D141       |
| 6   | D07        | 31  | D151       |
| 7   | -CSO       | 32  | -CS11      |
| 8   | A102       | 33  | -VS1       |
| 9   | -ATASEL    | 34  | - IORD     |
| 10  | A092       | 35  | -IOWR      |
| 11  | A082       | 36  | -WE3       |
| 12  | +3.3V      | 37  | INTRQ      |
| 13  | VCC        | 38  | VCC        |
| 14  | A062       | 39  | -CSEL      |
| 15  | A052       | 40  | -VS2       |
| 16  | A042       | 41  | -RESET     |
| 17  | A032       | 42  | IORDY      |
| 18  | A02        | 43  | - INPACK   |
| 19  | A01        | 44  | -REG3      |
| 20  | A00        | 45  | -DASP      |
| 21  | D00        | 46  | -PDIAG     |
| 22  | D01        | 47  | D081       |
| 23  | D02        | 48  | D091       |
| 24  | -IOCS16    | 49  | D101       |
| 25  | -CD2       | 50  | GND        |

79

# PCI BUS PIN ASSIGNMENT

The PCI-BUS edge connector is divided into two sets: one consists of 98-pin; the other consists of 22-pin. The pin assignments are as follows :

| F62 | F52 F49    |     |            |     |             |      | F1         |
|-----|------------|-----|------------|-----|-------------|------|------------|
|     |            |     |            |     |             |      |            |
| E62 | E52 E49    |     |            |     | COMPONENT S | SIDE | E1         |
|     | F          |     | Е          |     | F           |      | E          |
| PIN | ASSIGNMENT | PIN | ASSIGNMENT | PIN | ASSIGNMENT  | PIN  | ASSIGNMENT |
| F1  | -12V       | E1  | TRST#      | F31 | +3.3V       | E31  | AD18       |
| F2  | TCK        | E2  | +12V       | F32 | AD17        | E32  | AD16       |
| F3  | GND        | E3  | TMS        | F33 | C/BE2#      | E33  | +3.3V      |
| F4  | TDO        | E4  | TDI        | F34 | GND         | E34  | FRAME#     |
| F5  | +5V        | E5  | +5V        | F35 | IRDY#       | E35  | GND        |
| F6  | +5V        | E6  | INTA#      | F36 | +3.3V       | E36  | TRDY#      |
| F7  | INTB#      | E7  | INTC#      | F37 | DEVSEL#     | E37  | GND        |
| F8  | INTD#      | E8  | +5V        | F38 | GND         | E38  | STOP#      |
| F9  | REQ3#      | E9  | CLKC       | F39 | LOCK#       | E39  | +3.3V      |
| F10 | REQ1#      | E10 | +5V(I/O)   | F40 | PERR#       | E40  | SDONE      |
| F11 | GNT3#      | E11 | CLKD       | F41 | +3.3V       | E41  | SBO#       |
| F12 | GND        | E12 | GND        | F42 | SERR#       | E42  | GND        |
| F13 | GND        | E13 | GND        | F43 | +3.3V       | E43  | PAR        |
| F14 | CLKA       | E14 | GNT1#      | F44 | C/BE1#      | E44  | AD15       |
| F15 | GND        | E15 | RST#       | F45 | AD14        | E45  | +3.3V      |
| F16 | CLKB       | E16 | +5V(I/O)   | F46 | GND         | E46  | AD13       |
| F17 | GND        | E17 | GNTO#      | F47 | AD12        | E47  | AD11       |
| F18 | REQ0#      | E18 | GND        | F48 | AD10        | E48  | GND        |
| F19 | +5V(I/O)   | E19 | REQ2#      | F49 | GND         | E49  | AD09       |
| F20 | AD31       | E20 | AD30       | F52 | AD08        | E52  | C/BEO#     |
| F21 | AD29       | E21 | +3.3V      | F53 | AD07        | E53  | +3.3V      |
| F22 | GND        | E22 | AD28       | F54 | +3.3V       | E54  | AD06       |
| F23 | AD27       | E23 | AD26       | F55 | AD05        | E55  | AD04       |
| F24 | AD25       | E24 | GND        | F56 | AD03        | E56  | GND        |
| F25 | +3.3V      | E25 | AD24       | F57 | GND         | E57  | AD02       |
| F26 | C/BE3#     | E26 | GNT2#      | F58 | AD01        | E58  | AD00       |
| F27 | AD23       | E27 | +3.3V      | F59 | +5V(I/O)    | E59  | +5V(I/O)   |
| F28 | GND        | E28 | AD22       | F60 | ACK64#      | E60  | REQ64#     |
| F29 | AD21       | E29 | AD20       | F61 | +5V         | E61  | +5V        |
| F30 | AD19       | E30 | GND        | F62 | +5V         | E62  | +5V        |



APPENDIX

This section introduce you the maps concisely.

Section includes:

- Block Diagram
- Interrupt Map
- RTC & CMOS RAM Map
- Timer & DMA Channels Map
- I / O & Memory Map

# **BLOCK DIAGRAM**



## **INTERRUPT MAP**

| IRQ | ASSIGNMENT                          |
|-----|-------------------------------------|
| 0   | System TIMER interrupt from TIMER-0 |
| 1   | Keyboard output buffer full         |
| 2   | Cascade for IRQ 8-15                |
| 3   | Serial port 2 / Modem               |
| 4   | Serial port 1                       |
| 5   | Parallel port 2 / Sound Blaster     |
| 6   | Floppy                              |
| 7   | Parallel port 1                     |
| 8   | RTC clock                           |
| 9   | Available                           |
| 10  | COM4                                |
| 11  | COM3                                |
| 12  | PS/2 Mouse                          |
| 13  | Math coprocessor                    |
| 14  | IDE1                                |
| 15  | IDE2                                |

# **RTC & CMOS RAM MAP**

| CODE  | ASSIGNMENT                              |
|-------|---|
| 00    | Seconds                                 |
| 01    | Second alarm                            |
| 02    | Minutes                                 |
| 03    | Minutes alarm                           |
| 04    | Hours                                   |
| 05    | Hours alarm                             |
| 06    | Day of week                             |
| 07    | Day of month                            |
| 08    | Month                                   |
| 09    | Year                                    |
| 0A    | Status register A                       |
| 0B    | Status register B                       |
| 0C    | Status register C                       |
| 0D    | Status register D                       |
| 0E    | Diagnostic status byte                  |
| 0F    | Shutdown byte                           |
| 10    | Floppy Disk drive type byte             |
| 11    | Reserve                                 |
| 12    | Hard Disk type byte                     |
| 13    | Reserve                                 |
| 14    | Equipment byte                          |
| 15    | Base memory low byte                    |
| 16    | Base memory high byte                   |
| 17    | Extension memory low byte               |
| 18    | Extension memory high byte              |
| 30    | Reserved for extension memory low byte  |
| 31    | Reserved for extension memory high byte |
| 32    | Date Century byte                       |
| 33    | Information Flag                        |
| 34-3F | Reserve                                 |
| 40-7f | Reserved for Chipset Setting Data       |

## **TIMER & DMA CHANNELS MAP**

#### **<u>Timer Channel Map</u>** :

| Timer Channel | Assignment             |
|---------------|------------------------|
| 0             | System timer interrupt |
| 1             | DRAM Refresh request   |
| 2             | Speaker tone generator |

## **DMA Channel Map** :

| DMA Channel | Assignment                   |
|-------------|------------------------------|
| 0           | Available                    |
| 1           | Available / Sound Blaster    |
| 2           | Floppy                       |
| 3           | Available / ECP              |
| 4           | Cascade for DMA controller 1 |
| 5           | Available                    |
| 6           | Available                    |
| 7           | Available                    |

## I/O & MEMORY MAP

#### <u>Memory Map</u> :

| MEMORY MAP      | ASSIGNMENT                           |
|-----------------|--------------------------------------|
| 0000000-009FFFF | System memory used by DOS and        |
|                 | application                          |
| 00A0000-00BFFFF | Display buffer memory for VGA/ EGA / |
|                 | CGA / MONOCHROME adapter             |
| 00C0000-00DFFFF | Reserved for I/O device BIOS ROM or  |
|                 | RAM buffer.                          |
| 00E0000-00EFFFF | Reserved for PCI device ROM          |
| 00F0000-00FFFFF | System BIOS ROM                      |
| 0100000-FFFFFFF | System extension memory              |

## <u>I/O Map</u> :

| I/O MAP | ASSIGNMENT                              |
|---------|---|
| 000-01F | DMA controller (Master)                 |
| 020-021 | Interrupt controller (Master)           |
| 022-023 | Chipset controller registers I/O ports. |
| 040-05F | Timer control regsiters.                |
| 060-06F | Keyboard interface controller (8042)    |
| 070-07F | RTC ports & CMOS I/O ports              |
| 080-09F | DMA register                            |
| 0A0-0BF | Interrupt controller (Slave)            |
| 0C0-0DF | DMA controller (Slave)                  |
| 0F0-0FF | Math coprocessor                        |
| 1F0-1F8 | Hard Disk controller                    |
| 278-27F | Parallel port-2                         |
| 2B0-2DF | Graphics adapter controller             |
| 2F8-2FF | Serial port-2                           |
| 360-36F | Net work ports                          |
| 378-37F | Parallel port-1                         |
| 3B0-3BF | Monochrome & Printer adapter            |
| 3C0-3CF | EGA adapter                             |
| 3D0-3DF | CGA adapter                             |
| 3F0-3F7 | Floppy disk controller                  |
| 3F8-3FF | Serial port-1                           |



| No | Assignment                    | Photo |
|----|-------------------------------|-------|
| 01 | COM2<br>COM PORT<br>CONNECTOR |       |

| 02 | J1<br>Power LED & Hard<br>Disk Drive LED<br>Connector                                   |  |
|----|---|--|
| 04 | Touch panel control<br>board<br>Link to touch panel.                                    |  |
| 05 | Touch panel control<br>board<br>Link to main board.<br>Power line and RS-232<br>signal. |  |



| 9  | INVERTER POWER<br>CONNECTOR<br>Link to inverter board.                       |  |
|----|--|--|
| 11 | A: JP13<br>LVDS PANEL<br>VOLTAGE SELECTION<br>B: MS1<br>PS/2 Mouse Connector |  |

| A | Touch panel connector<br>with touch panel<br>control line. |  |
|---|--|--|
| В | Inverter board<br>Link to main board<br>Support DC power.  |  |
| С | CPU FAN CONNECTOR  |  |

## **Resolution Setting for MS XPe Installation.**

Because LCD module resolution is 640x480 but the MS XP default resolution is 800x600, use may not see some icons during MS XP OS installation. And, below are the methods for user to install MS XP OS.

- 1. Equipment Request: Monitor with VGA interface Keyboard/Mouse XPe CD-ROM
- 2. Installation process:
- 2.1 Plug VGA monitor to VGA port
- 2.2 Plug Keyboard/mouse to PS2 port
- 2.3 Plug HDD and CD-ROM to IDE port
- 2.4 Turn on power switch
- 2.5 Press Del key to setup BIOS
- 2.6 Select "Advance Chipset Features"

| Standard CNOS Features    | Frequency/Voltage Control |       |
|---------------------------|---------------------------|-------|
| Advanced BIOS Features    | Load Fail-Safe Defaults   |       |
| Advanced Chinset Features | Lond Antinized Refaults   |       |
|                           | Load optimized perduits   |       |
| Integrated Peripherals    | Set Password              |       |
| Power Management Setup    | Save & Exit Setup         |       |
| PnP/PCI Configurations    | Evit Without Saulan       |       |
| P. Hantah Status          |                           |       |
| Fic health Status         |                           |       |
| Esc : Quit                | 1 4 + + : Select Item     |       |
| F10 : Save & Exit Setup   |                           |       |
| 2.7 Select "Panel Type"   |                           |       |
| DRAM Timing By SPD        | [Enabled]                 |       |
| x BRAM Clock              |                           |       |
| x SDRAM Cycle Length      |                           |       |
|                           | Disabled                  |       |
| Memory Hole               | [Disabled]                |       |
| P2C/C2P Concurrency       | [Enabled]                 |       |
| System BIOS Cacheable     | [Disabled]                |       |
| Frame Buffer Size         | [16M]                     |       |
| Select Display Device     | [CRT+LCD]                 |       |
| IV Iype                   | [JP NTSC]                 |       |
| Panel Type                | [LVDS 640x480             | 185it |
| UnChip USB                | [Enabled]                 |       |
| USB Reyboard Support      | [Disabled]                |       |
| CPU to PCI unite Bucc     | LAutol                    |       |
| PCI Bupanic Bunction      | F LENADICAJ               |       |
| PCI Master 0 WS Write     | [Enabled]                 |       |
| PCI Delay Transaction     | [Disabled]                |       |
|                           |                           |       |

2.8 Change setting to "LVDS 800\*600 18bit"

| Panal Tuna  | x Bank Interleave<br>Memory Hole                                       | J<br>Disabled<br>[Disabled]          |
|---|--|--------------------------------------|
|   | P2C/C2P Concurrency<br>System BIOS Cacheable                           | [Enabled]<br>[Disabled]              |
| LVDS 10024x768 36bit []                           | Frame Buffer Size<br>Select Display Device                             | [16M]<br>[CRT+LCD]                   |
| LVDS 1024x768 18bit [ ]<br>LVDS 640x480 18bit [ ] | Panel Type<br>OnChip USB   | [LVDS 800x600 18bit<br>[Enabled]     |
| LVDS 1400x1050 36bit [ ]                          | USB Keyboard Support<br>OnChip Sound                                   | [Disabled]<br>[Auto]                 |
| 14:Nove ENTER:Accept ESC:Abort                    | PCI Dynamic Bursting<br>PCI Master 0 WS Write<br>PCI Delay Transaction | [Enabled]<br>[Enabled]<br>[Disabled] |

2.9 Press F10 key to save setting

2.10 Boot from CD-ROM to start Windows XP installation 2.11 Use VGA monitor to install Windows XP

#### WLM-6581-06 LCD monitor

# VGA Monitor Screen

| Windows <sup>10</sup>                            | Nindows <sup>10</sup>                  |
|--|--|
| 歡迎使用 Microsoft Windows                           | 武即使用 Microsoft Windows                 |
| EXTRA Acceleration (* -<br>Britsenderstrational) |  |
|  |  |
|  |  |
|  |  |
|  | ************************************** |

Remark: We can see NEXT button in VGA monitor screen which is not shown in WLM-6581-06 LCD monitor.

2.12 Windows XP installation finished.

- 2.13 Reboot system
- 2.14 Press Del key to setup BIOS
- 2.15 Select "Load Optimized Defaults"





2.18 Press y 2.19 Xpe installation finished